



**ISOCC 2024** August 19(Mon.)~22(Thur.), 2024  
21<sup>st</sup> International SoC Design Conference Premier Hotel Tsubaki, Sapporo, Japan

## ✓ ABOUT ISOCC

ISOCC has established a long tradition as an annual conference providing the premier SoC design forum for worldwide researchers from academia and industries. Since its inception, ISOCC has been continuing to showcase the most recent innovations and trends in the semiconductor system-on-a-chip area with active participations from worldwide researchers in academia, industry, and institutes. ISOCC 2024 welcomes technical papers in the field of semiconductor circuits and systems presenting new advanced concept and developments in analog and digital circuit and system design, theory, simulation, modeling, experimental implementations and experiences, and emerging technologies in the system-on-a-chip area.

The 21st International SoC Conference (ISOCC 2024) will be held from August 19 to 22, 2024 at the Premier Hotel Tsubaki, Hokkaido, Sapporo, Japan. Sapporo, located in the southwestern part of Hokkaido island, is Japan's northernmost ordinance-designated city. It covers roughly the same area as Hong Kong and is home to a growing population of more than 1.95 million. Sapporo is one of the few cities in Japan to have a mild and humidity-free summer, and in the winter it can see snowfall amounts reach upwards of five meters. This climate provides the city with unique and sharp contrasts in each of the four seasons.

ISOCC 2024 is financially co-sponsored by IEEE CAS Society. All accepted papers will be published in the conference proceedings and will be submitted for inclusion in IEEE Xplore. We also welcome proposals for tutorials and special sessions.

We look forward to seeing you in Sapporo.

## ✓ TOPICS OF INTEREST

Topics include, but are not limited to:

### - Analog Circuits

- Analog Circuits
- Amplifiers and Filters
- Power Management Circuits

### - Data Converters

- Analog-to-Digital Converter
- Digital-to-Analog Converters
- Analog Circuits for Data Converters

### - Digital Circuits, Architecture, and Systems

- Multimedia Systems and Image Processing Applications
- Digital Signal Processing and Communication Systems
- Embedded Software and Systems
- Hardware Security Circuits
- Memory Circuits and Systems

### - RF/Microwave/Wireless

- RF Circuits and Transceivers
- Microwave and Millimeter-Wave Circuits
- Wireless Communication Circuits

### - Wireline

- High-Speed Interface
- Wireline Link

### - Machine Learning and AI

- Machine Learning/AI Algorithms
- Systems and Architectures for Machine Learning/AI
- Machine Learning Accelerators and Circuits
- Near-Memory and In-Memory Computation for AI
- Neuromorphic Architectures and Processors

### - SoC Design Methodology and CAD

- Software and Algorithm
- HW-SW Co-Design
- Embedded SoC and FPGA Design
- SoC Testing and Design Verification
- Reliability in Circuits and Systems
- Modeling and Simulation

### - Circuits and Systems for Emerging Technologies

- Neuromorphic Devices and Circuits
- Sensory Circuits and Systems
- Biomedical Circuits and Systems
- Automotive Circuits and Systems
- IoT/IoE Circuits and Systems
- 3-D ICs and SoC Packages
- Emerging Device Based Circuits

## ✓ IMPORTANT DATES

- Submission Due date of Special Session and Tutorial Proposals: **April 3, 2024**
- Notification of Acceptance of Special Session and Tutorial Proposals: **April 10, 2024**
- Submission Due Date of Regular Session and Special Session Full Papers: **April 24, 2024**
- Notification of Acceptance of Regular Session and Special Session Full Papers: **June 12, 2024**
- Author & Early Registration: **July 3, 2024**
- Submission of Final Papers (for all accepted papers): **July 3, 2024**
- On-site Registration: **August 19, 2024**

## ✓ PAPER SUBMISSION

A complete 2-page manuscript must be submitted electronically in PDF format (in Standard IEEE double-column format posted on the conference website). Only electronic submissions will be accepted. Accepted papers will be submitted for inclusion into IEEE Xplore subject to meeting IEEE Xplore's scope and quality requirements. For more information, please refer to the conference website: <http://www.isocc.org>

## ✓ International Organizing Committee

### General Chair

- **Seokhyeong Kang** (POSTECH, Korea)

### General Co-Chairs

- **Yoshikazu Miyanaga**  
(Chitose Institute of Science and Technology, Japan)

### General Vice Chair

- **Yong Sin Kim** (Korea University, Korea)

### Conference Management Chairs

- **Won-Young Lee** (SEOULTECH, Korea)
- **Sunmean Kim** (Kyungbuk National University, Korea)

## ✓ Technical Program Committee

### Technical Program Chair

- **Youngmin Kim** (Hongik University, Korea)

### Technical Program Co-Chair

- **Yoshifumi Nishio** (Tokushima University, Japan)

### Technical Program Vice Chairs

- **Suk-Ju Kang** (Sogang University, Korea)
- **Jerald Yoo** (National University of Singapore, Singapore)

## ✓ Premier Hotel -Tsubaki- Sapporo



Address: 1 Chome-1-1 Toyohira 4 Jo, Toyohira Ward, Sapporo, Hokkaido

Website: <https://premier.kenhotels.com/sapporo/>

Tel: +81-11-821-1111

## ✓ Secretariat

Address: 1736, Dukmyung Building, 625, Teheran-ro, Gangnam-gu, Seoul, Korea, 06173

Tel: +82-2-757-0981

E-mail: [secretary@isocc.org](mailto:secretary@isocc.org)