YOU ARE INVITED

Continental
The Future in Motion

Asia Product
Cybersecurity and Privacy
Summit 2024

| APRIL 17

Multi-Purpose Hall, Continental Building

Product Cybersecurity and Privacy Office
Continental Automotive

Our global technology collaboration forum Cybersecurity Summit is coming to Singapore.

We are excited together with our customers, partners, experts' community, and Continental executives for this meet on **17 April 2024** at Continental Singapore. The Future is Now, and collaboration makes the difference.

At this year's Product Cybersecurity & Privacy Summit, you can expect to:

- Discuss and exchange ideas through thought provoking panel discussions.
- Explore the latest product Cybersecurity and best-in-class solutions.
- Discover more on the innovation & start-up ecosystem within Asia-Pacific region and how Continental shapes the future of technology with partners.

Time (SGT)	Agenda
8.30 - 9.30	*Registration and Power Breakfast*
9.30 - 9.35	Welcome address by Dr Estelle Wang, Head of Product Cybersecurity & Privacy Office, SCT, Continental Automotive Singapore
9.35 - 9.40	Opening message by KF Lo, President, Continental Automotive Singapore
9.40 - 9.45	Introduction by Professor Yuanjin Zheng, Chair, IEEE CAS SG Chapter
9.45 – 10.15	**Keynote 1: Cybersecurity on mobility by Michael Kasper** CEO of Fraunhofer Singapore Research Ltd Q&A
10.15 -10.45	**Keynote 2: Post-Quantum cryptography in automotive security by Ruben Niederhagen** Associate Research Fellow at the Institute of Information Science at Academia Sinica in Taiwan & Associate Professor at the Department of Mathematics and Computer Science at the University of Southern Denmark Q&A
10.45 -11.00	*Coffee break*
11.00 -11.30	**Keynote 3: Autonomous framework on AI by Niels de Boer** Chief Operating Officer, Energy Research Institute @ NTU; Senior Programme Direction for Center of Excellence for Testing & Research Autonomous Vehicles Q&A
11.30 -12.00	**Keynote 4: IEEE standards for AI/Autonomous Driving by Ravi Subramaniam** Senior Director (Acting), Global Business Strategy and Intelligence at the IEEE Standards Association, USA Q&A
12.00 -13.45	Networking Luncheon

DR. YI (ESTELLE) WANG

Head of Product Cybersecurity & Privacy Office (PCSO) Singapore
Software & Central Technologies, Continental Automotive Singapore

Dr. Yi (Estelle) Wang has been working as Head of Product Cybersecurity & Privacy Office (PCSO) Singapore at Continental Automotive since July 2016 (with a 2008 Ph.D. degree from Nanyang Technological University, Singapore). Dr. Wang is leading three teams: the automotive advanced research and governance team, consulting and engineering team, and the security operations team. In addition, she is with deep knowledge of ISO/SAE 21434 and UNECE R155/R156 based on physically participating in working group meetings. She is an expert with cybersecurity industrial standards, e.g., ISO 2700x and ISO 26262. She is familiar with data protection and privacy regulations: GDPR and Asia regional regulations (Singapore PDPA and Chinese PIPL). Advanced leading research topics are including but not limited to applied post quantum cryptography, applied lightweight cryptography, cybersecurity for artificial intelligence (adversarial model), embedded security for IoTs, data anonymization for personal privacy.

Dr. Wang is honored with "Top Women in Security ASEAN 2021" 2021 and 2022 (Singapore, Phillips, Indonesia, Malaysia, Cambodia, and Thailand), "Top Women in Security – Singapore Country Award" and "Top 20 Women in Cybersecurity Singapore 2020" by Public Security Society. She brought the know-how to the Singapore Cybersecurity ecosystem through guest lectures with the Singapore Institute of Technology in 2019. She is motivated to be valuable in various domains, actively contributing to her company, the IEEE non-profit community, and the Singapore eco-system. She firmly believes that investing effort in mentoring and coaching young talents will contribute to creating a better future.

With more than 20+ years of experience in various embedded security topics, she is active in academic activities, has published more than 53 international top journal/conference papers, and holds 13 industrial patents. She was Chair of the IEEE Circuit and System Society (CASS) chapter Singapore 2022-2023, Associate Editor of IEEE Transactions on Circuit and System II 2020-2022, and Chair for IEEE Standard of Hardware Security under IEEE Circuits and Systems Society Standards Activities Subdivision.

MR. LO KIEN FOH

President & CEO, Continental Automotive Singapore Pte Ltd

Mr Lo Kien Foh, born in 1964, joined Philips Car Systems as an Electronic Engineer after graduating with a Bachelor Engineering from the Nanyang Technological Institute in 1988. He marked his early career in the R&D arena, working on schematic circuit, ranging from analogue & power circuitry to digital signal analysis. At the same time, he expanded his scope to perform PCB layout; starting with single and later multilayer design. He was also working closely with the automotive OEM customers in Australia, transforming their requirements to product realization by performing in-depth joint field tests with the customers. As a passionate engineer, he moved on to obtain his Master of Engineering in 1994.

Mr Lo progressed to become the Director of R&D in 2004, and Vice-President of R&D in 2006, where he led the teams in Singapore and China, to design and develop automotive products for the worldwide market. In 2007, his portfolio further expanded where he was responsible for the operation of the company as a COO, overseeing the complete life cycle of all programs, projects, quality, processes, purchasing, human resources as well as finance & controlling. To broaden his knowledge, he successfully obtained a Master of Finance in 2010. He took over the Singapore office and became the Managing Director of Continental Automotive Singapore in 2010, where he also served as a director of the Board.

In 2019, he was promoted to President & CEO of Continental Automotive Singapore.
Mr Lo sits in a few advisory boards of SIT (Singapore Institute of Technology) and TUM CREATE (Technical University of Munich). He is also a member of CARTS (Committee on Autonomous Road Transport in Singapore) spearheaded by the Singapore Ministry of Transport. Mr. Lo was appointed as the Country Head of Continental Singapore on 1st January 2019. Since 1 st April 2019, Mr. Lo is a member of the Nanyang Technology University Board of Trustees. Mr Lo is a Singaporean and is married with a son.

RAVI SUBRAMANIAM

Ravi Subramaniam is currently serving as **Senior Director (Acting), Global Business Strategy and Intelligence at the IEEE Standards Association**. In this role he oversees multiple business lines and is also responsible for marketing and product development. He also advises and consults for numerous standards working groups on post standards implementation which encompasses test plan, certification program development and interoperability. Currently he is focused on developing programs in interconnection of distributed energy resources, AI Ethics, Digital Resiliency and cybersecurity, Autonomous Vehicles and sensors/IoT technologies. He has helped form and support various IEEE certification programs and holds leadership roles within numerous committees, including the IEEE Sensors Council Industry Committee and the nuclear standards certification committee.

Prior to taking on this role, Ravi has served in various technology management roles across the globe. He has previously worked for Eurofins E&E North America, Honeywell and Ericsson. Ravi studied Electrical Engineering at Arizona State University and Johns Hopkins University and has an executive MBA from Rutgers University.

https://www.linkedin.com/in/rsubramaniam/

DR. ZHENG YUANJIN

Full Professor, School of EEE, Nanyang Technological University

Dr. Zheng Yuanjin received his B.Eng. from Xi'an Jiaotong University, P.R. China in 1993, Master Engineering from Xi'an Jiaotong University, P. R. China in 1996, and Ph.D. from Nanyang Technological University, Singapore in 2001.

From July 1996 to April 1998, he worked at the National Key Lab of Optical Communication Technology, University of Electronic Science and Technology of China. He joined the Institute of Microelectronics, A*STAR in 2001 as a senior research engineer, and then promoted to a principal investigator and group leader for the wideband RFIC design group. Here, he has led and developed various CMOS RF transceivers and baseband SoC for WLAN, WCDMA, Ultra-wideband, and low power medical radio etc. In July 2009, he joined Nanyang Technological University. He has been working on electromagnetic and acoustics physics and sensors, biomedical imaging especially photoacoustic / thermoacoustic imaging and 3D imaging, deep learning etc.

Dr. Zheng is an internationally well-recognized researcher on ultra-wideband transceiver (UWB) and radar transceiver integrated circuit (IC). He has pioneered UWB IC research, published the world first paper of UWB pulse generator IC paper and second most cited paper of UWB transceiver IC. He has consistently published at ISSCC (the world top conference and land marker in the area of solid-state circuits), keeping the record of the first and most ISSCC papers (7) published in Singapore. Dr. Zheng has published more than 360 journal and conference papers, 27 patents filed/granted and 5 book chapters, including high profile articles of Advanced Materials, Nature Communications, Materials Horizon, Nano Energy, Advanced Functional Materials, Small, Scientific Reports etc. He served as session chairs and TPC chairs/members for several international conferences. He has successfully led and contributed numerous public funded research and industry projects. He was accredited with the excellent thesis award, Xi'an Jiaotong university in 2006; Ahmed Elsaify Memorial Award for Best Paper at IEEE International Conference on Body Sensor Networks, Singapore, 2010; Best paper award of ISCAS biomedical and Life science track, 2018. He was a finalist of best paper award IMS-S, and IEE TSS design competition merit award winner.

Prof. Zheng is active in the service for the research community both internally and externally. He serves as Director of VIRTUS IC Design of Excellence, NTU and Program Director of VALENS Centre of Excellence for Bio-Instrumentation, Devices and Signal Processing, NTU. He is currently an associate editor of Journal of Circuits, Systems & Signal Processing, Journal of X-Acoustics: Sensing and Imaging, and IEEE Trans. on Biomedical Circuit and Systems. He has been organizing over 10 conferences as TPC and session chairs and has delivered over 20 invited talks at international conferences. He is a senior member of IEEE and member of SID and SPIE.

MICHAEL KASPER

Michael is **Chief Executive Officer (CEO) of Fraunhofer Singapore Research Ltd** and Executive Director of the Fraunhofer Centre for Applied Security (FSR-CAIS). He collaborates with the Nanyang Technological University (NTU) and de facto co-heads the Collaborative Research Centre FSR@NTU and holds an adjunct associate professorship appointment with Nanyang Technological University, School of Computer Science and Engineering (SCSE), College of Engineering (CoE). At Fraunhofer, Michael and his team work on R&D of novel solutions and their translation from fundamental research into innovation and applications in relevant domains. At Fraunhofer Singapore and NTU he oversees and is working with his teams on new directions in diverse field of applied and translational R&D, in the sector of cyber security, artificial intelligence and cognitive technologies, and sustainable ocean economy and blue bioresources. Passionate on bridging and strengthening the collaboration in applied research between Singapore and Germany, he supports governmental, academic, and industrial initiatives on an operational and strategical level.

Michael is affiliated with the Quantum Engineering Programme (QEP) of the National University of Singapore (NUS), Singapore, where he is a coordinator for the National Quantum-Safe Network (NQSN) activities. He is adjunct faculty at GLOBIS university at Japan's largest MBA university, where he teaches Cybersecurity for Business MBA in Tokyo in their Technovation program.

Previously, Michael co-headed the department Cyber-Physical Systems Security at the Fraunhofer Institute for Secure Information Technology (SIT) and headed the Hardware Security Lab, responsible for security testing and evaluation of cross-industry prototypes. In the past, Michael was affiliated with the German National Research Center for Security and Privacy ATHENE, where he was responsible for the cross-institutional management of the research area "Secure Things" in the center's institution's predecessor CASED. Since 2022 he is affiliated with the Fraunhofer Institute for Applied and Integrated Security (AISEC) in Munich and Fraunhofer Institute for Electronic Nanosystems (ENAS).

Michael has more than 25-year professional experience in cyber security, cryptographic engineering, and computer science and cross-domain background in electrical engineering, microelectronics, data analytics, artificial intelligence, optics and applied physics, translational research and business management.

RUBEN NIEDERHAGEN

Ruben Niederhagen is **Associate Research Fellow at the Institute of Information Science at Academia Sinica in Taiwan and Associate Professor at the Department of Mathematics and Computer Science at the University of Southern Denmark** in Denmark. He obtained his PhD at Eindhoven University of Technology in the Netherlands in 2012.

His research fields are Applied and Embedded Cryptography as well as Post-Quantum Cryptography. He is co-submitter of the hash-based signature scheme SPHINCS+ that has been selected by NIST for standardization, of the code-based key-encapsulation scheme Classic McEliece that is currently in discussion by NIST for standardization, and of the code-based signature schemes MEDS and WAVE that are currently in the 2nd NIST PQC standardization process.