IEEE Open Journal of Circuits and Systems
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Call for Papers

Special Section on High-Speed Wireline and Optical Communication Circuits and Systems
Submission date: July 26th 2020, Publication date: December 2020

Motivation
The explosive growth of data traffic requires advances in both wireline and optical communication circuits with ever-increasing data rate. To promote techniques in both circuit and system levels to tackle various challenges as the data rate keeps increasing and provide insightful guidelines for interconnects in the years to come, OJCAS is introducing a Special Section focused on High-Speed Wireline and Optical Communication Circuits and Systems. Innovative and original submissions are solicited in areas including (but not limited to) the following:

- Receivers/transmitters/transceivers for intra-chip, chip-to-chip, and 2.5/3D interconnections, memory and graphics interfaces, backplanes, optical links, and long-haul communications
- Innovative I/O circuits for advancing data rates and improving power efficiency
- Clock generation and distribution including PLLs and CDRs
- Building blocks such as equalizers, ADC/DAC-based transceivers, silicon photonic and optical interface circuitry

The Publication Fees/Article Processing Charges (APCs) for the accepted papers will be completely subsidized by the CAS Society. Hence these publications will be completely free of charge to the authors.

Publication Schedule
Manuscript submission deadline: July 26th, 2020
First-round revision notification due: Sept. 6th, 2020
Revised manuscripts due: Sept. 27th, 2020
Second-round revision notification due: Oct. 25th, 2020
Final manuscript due: Nov. 22nd, 2020
Online publication: Dec. 2020

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Instructions for authors: Manuscripts must be submitted online using the IEEE OJCAS Manuscript Template via Manuscript Central at: https://mc.manuscriptcentral.com/oj-cas. During submission, the answer to question “Type” in Step 1 of the submission process, is “High-Speed Communication”.

IEEE Open Journal of Circuits and Systems is a new gold fully open access journal that publishes high-quality, peer-reviewed papers covering the theory, analysis, design, tools, and implementation of circuits and systems. This includes their theoretical foundations, applications, and architectures, as well as circuits and systems implementation of algorithms for signal and information processing.