SoC Tech Community Outreach Enrichment in IEEE SOCC 2014 Program

IEEE CAS Outreach Initiative 2014 Report

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Description of Sponsored Activity

Sponsored by IEEE CASS Outreach Initiative 2014, a series of outreach activities have been organized for the SOC/VLSI tech community correlated with 2014 IEEE SoC Conference program (http://socc2014.ieee-socc.org/), mainly the SOCC Student Grants program and a full-day SoC Tech Tutorial School.

The SOCC Student Travel Grants were offered for students all over the world to attend the SOCC 2014 conference and present their work. The grants partially covered the travel expenses of participating in the conference, such as economy airfare and lodging. The announcement of this grant opportunity was broadcasted at the conference website, in the Call for Paper, CASS website, CASS newsletters, various mailing lists, etc, which greatly motivated the graduate students to submit their work to the conference and compete for the travel award. All applications were required to submit their package by following the detailed eligibility requirements and application instructions. The students from the developing nations and underrepresented groups were received special consideration. Selected student authors were granted the travel awards according to their supporting status from their universities and research fundings.

The SoC Tech tutorial school was specially designed to encourage graduate students to join SoC research field and gain insight of research challenges and opportunities. The tutorial school featured a series of lectures from renowned industry experts and academic researchers. The full-day program is shown below in Table 1. The Call for Participation of tutorial school was advertised on the conference website, CASS website, CASS newsletters, various mailing lists, etc. Tutorial School Grants were offered to graduate students for participating in the full-day lectures on SoC research and development. The grant covered the student registration fees for the conference and tutorial. The detailed eligibility requirements and application instructions were provided to guide the students' applications and orient the selection process.

The CASS sponsorship were acknowledged in the message from the general chair in the conference proceedings, in the technical program, and at the conference website. The grant recipients attended the full-day tutorial school and the conference technical sessions, actively shared their experience by using social media (e.g., twitters, LinkedIn, Facebook). All awardees attended the post-conference surveys to provide their evaluation, feedback and concerns. Their evaluation and comments of the Initiative program were collected, summarized and shared among the organizing committee, enabling the future organizing team to better plan and execute future conferences and tutorial school and tailor them to meet attendees' needs.

It's worth mentioning that the Initiative grants greatly promoted the IEEE and CASS memberships. The IEEE CASS Membership Information Sector was setup at the conference and tutorial registration booth. The membership brochure were attached in the attendees' package. As the SOCC Student Grants required that the applicants must be student members of IEEE CAS Society, about 9 students joined IEEE and CASS student membership (among them, 5 were not even IEEE student members).

7:30am ~ 10:00am	Registration	
8:30AM – 10:00AM	*M. Tehranipoor* Univ. of Connecticut Opportunities and Challenges for Secure Hardware and Verifying Trust in Integrated Circuits	*P. Pande2, A. Nojeh1, A. Ivanov1* 1Univ. of British Columbia, Canada 2Washington State Univ Wireless NoC as Interconnection Backbone for Multicore Chips: Promises and Challenges
10:00AM – 10:30AM	Coffee break	
10:30AM – 12:00PM	*G.M. Blair* Avago Clock Implementation: A Question of Timing	*A. Nojeh1, P. Pande2, A. Ivanov1* 1Univ. of British Columbia, Canada 2Washington State Univ Carbon Nanotubes and Opportunities for Wireless On-Chip Interconnect
12:00PM – 1:30PM	Lunch break	
1:30PM – 3:00PM	*U.Y. Ogras* Arizona State Univ. Design and Management of Multiprocessor System-on-Chips	*H. Sun* Infinera Recent Advancements in Fiber Optic Transmission Enabled by Highly Integrated Mixed Signal SoC and Advanced Digital Signal Processing
3:00PM – 3:30PM	Coffee break	
3:30PM – 5:00PM	*A. Marshall* Univ. of Texas in Dallas System-on-Chip Design Using Tri-Gate Technology	*W. Kunz, D. Stoffel, J. Urdahl* Univ. of Kaiserslautem, Germany Formal Verification in System-on-Chip Design: Scientific Foundations and Practical Methodology

Table 1: Tutorial School program

Expense Summary

The amount of $16,000 was funded to conduct these outreach activities. A total of 16 full time master and Ph.D students who authored/co-authored an accepted paper at SOCC 2014, received the travel grants. Each award recipient received up to $1,035 ($535 for 7 US students) to partially cover their travel expenses. One Indian student with financial difficulty received exceptional grant of $1,405. The grant of 3 US recipients was around $300 each as their traveling cost is much less. 15 full-time graduate students received the tutorial grants to cover their tutorial school registration. Among them, 3 non-author applicants were also covered with conference registration to encourage their participation in the conference. The remaining funds were used to cover partial of the printing cost ($385), partial of the program cost ($935), and partial of the logistics cost ($185). All funding expenses were summarized in Table 2 below.

The tutorial school has attracted 52 attendees and received a total amount of $2,800 tutorial registration fees (speakers registrations were waived), which were used to cover the meal cost of luncheon and two coffee breaks for all attendees, setup cost of two meeting rooms, and on-site staff cost. The details of this part of income and expenses, please refer to the conference finance report submitted to the IEEE.

Expense Items	Description	Amount
Student travel grants	Partially cover travel expenses including airfare and lodging	$12,625
Tutorial school grants	cover conference and tutorial school registrations	$1,870
Printing	Tutorial School program booklet, Tutorial school SOCC student grant package, survey report, & IEEE CAS Membership Brochure	$385
Program	Tutorial School speaker plaques, gifts & participants certificates	$935
Logistics	Tutorial school banners & stand, poster and signs	$185
Total		**$16,000**

Table 2: Summary of Initiative grant expenses